

### Product Description

The Nxbeam NPA2040-MQ is a Ka-band high power amplifier fabricated in 0.2um GaN HEMT on SiC in a metal QFN package. The amplifier operates from 27.5 to 31 GHz and provides an average of 8 W saturated output power, 25% PAE, and 22 dB of linear gain. The metal QFN is designed for easy system integration with RF input and output ports matched to 50 ohms.



### Applications

- Ka-band Satellite Communications
- 5G Infrastructure
- Point-to-Point/Multipoint Digital Radios

### Key Features

- Frequency: 27.5 – 31 GHz
- Linear Gain (Ave.): 22 dB
- Psat (Ave.): 8 W
- PAE (Ave.): 25%

### Electrical Specifications

Test Condition:  $V_d = 26\text{ V}$ ,  $I_{dq} = 0.5\text{ A}$ , CW Performance, Typical Performance at 25°C

Parameter		Min	Typical	Max	Unit
Frequency		27.5		31	GHz
Gain (Small Signal)	27.5 GHz		21.8		dB
	29 GHz		23.1		
	31 GHz		23.3		
Output Power (at Psat, Pin=23.3 dBm)	27.5 GHz		37.9		dBm
	29 GHz		39.5		
	31 GHz		39.2		
PAE (at Psat, Pin=23.3 dBm)	27.5 GHz		21.0		%
	29 GHz		28.0		
	31 GHz		28.0		
Power Gain (at Psat, Pin=23.3 dBm)	27.5 GHz		16.2		dB
	29 GHz		17		
	31 GHz		17.2		
Input Return Loss	27.5 GHz		7		dB
	29 GHz		12		
	31 GHz		20		
Output Return Loss	27.5 GHz		5		dB
	29 GHz		12		
	31 GHz		17		

### Maximum Quiescent Bias

Parameter	Max	Unit
Drain Voltage (Vd1, Vd2, Vd3)	28	V
Drain Current (Id1)	140	mA
Drain Current (Id2)	160	mA
Drain Current (Id3)	550	mA

Maximum quiescent bias represents the operational bias used during reliability life testing. Biasing the part at or below this bias ensures reliability will be bound by the published reliability results.

### Absolute Maximum Ratings (Temp. = 25°C)

Parameter	Min	Max	Unit
Drain Voltage (Vd1, Vd2, Vd3)		28	V
Drain Current (Id1)		350	mA
Drain Current (Id2)		400	mA
Drain Current (Id3)		1400	mA
Gate Voltage (Vg1, Vg2, Vg3)	-8	0	V

Absolute maximum ratings represent the maximum current under power saturation conditions.

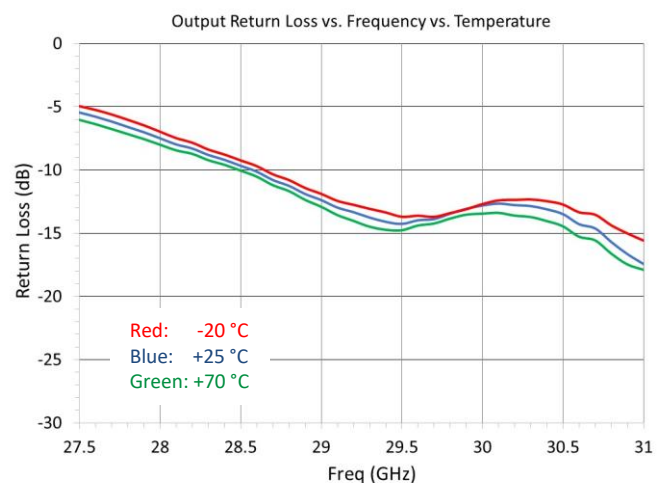
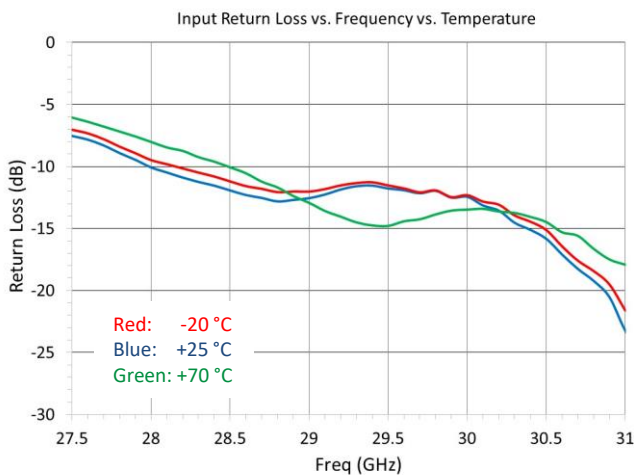
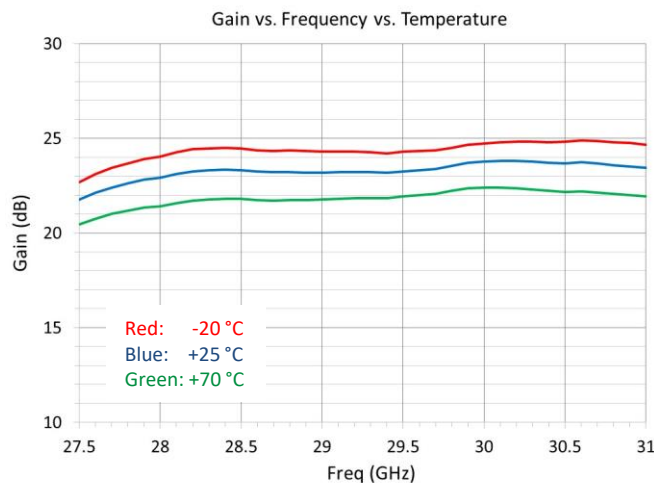
### Recommended Quiescent Operating Condition

Parameter	Value	Unit
Drain Voltage (Vd)	20 - 27	V
Drain Current (Id1)	up to 140	mA
Drain Current (Id2)	up to 160	mA
Drain Current (Id3)	up to 550	mA
Gate Voltage (Vg) (Typical Range)	-5.5 to -3.5	V

Gate voltage will vary based on desired current per stage

### Small Signal Performance

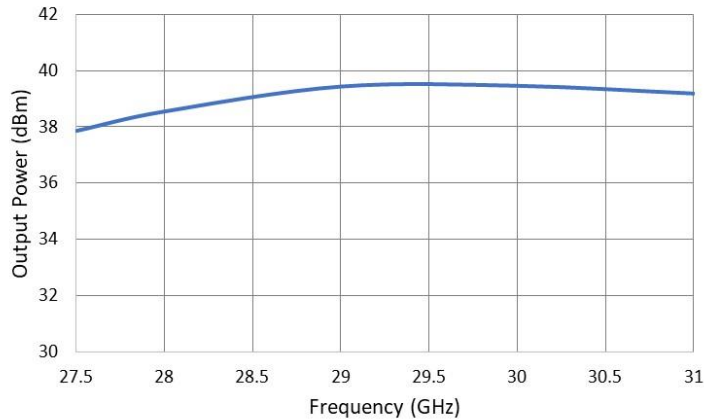
Test Condition: Vd = 26 V, Idq = 0.5 A, (CW Performance)



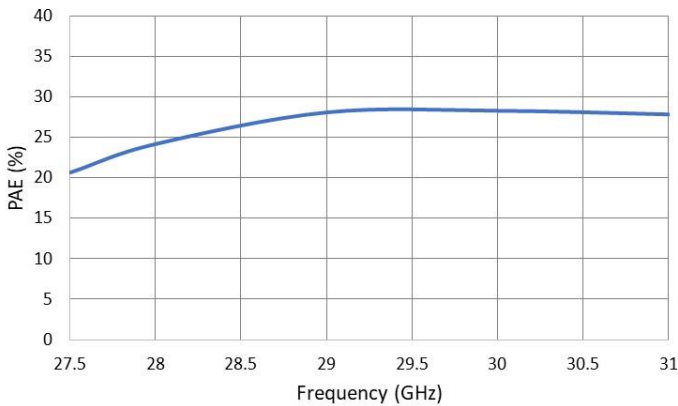
### Large Signal Performance

Test Condition:  $V_d = 26\text{ V}$ ,  $I_{dq} = 0.5\text{ A}$ ,  $P_{in} = 21.8\text{ dBm}$   
 (CW Performance, Typical Performance at 25°C)

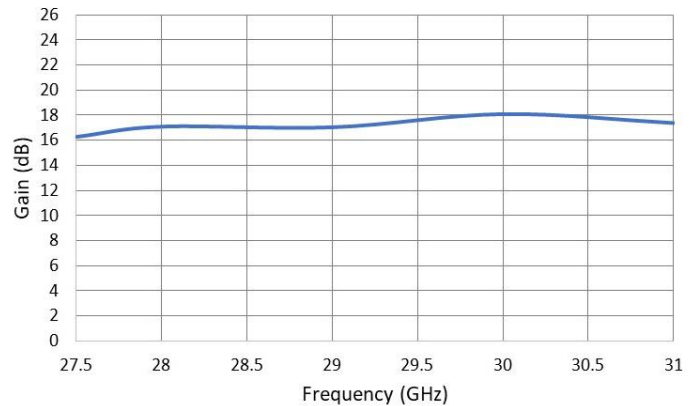
Output Power vs. Frequency (at 21.8 dBm Pin)



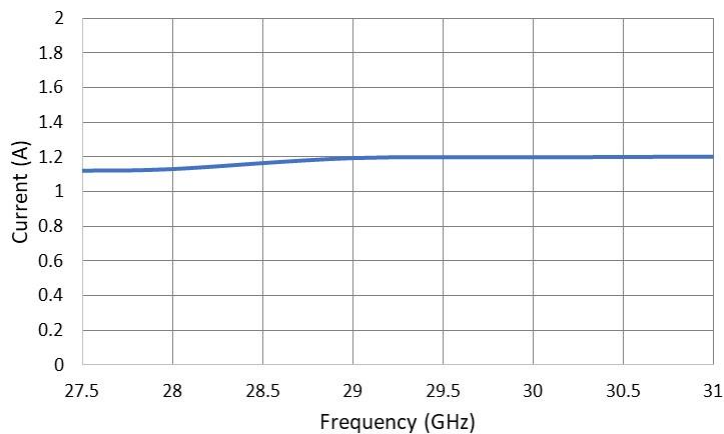
PAE vs. Frequency (at 21.8 dBm Pin)



Gain vs. Frequency (at 21.8 dBm Pin)



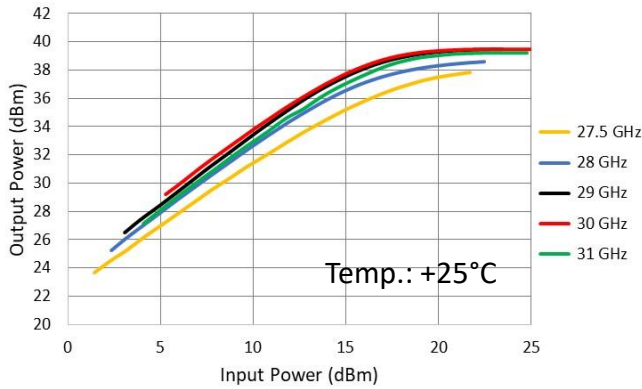
Drain Current vs. Frequency (at 21.8 dBm Pin)



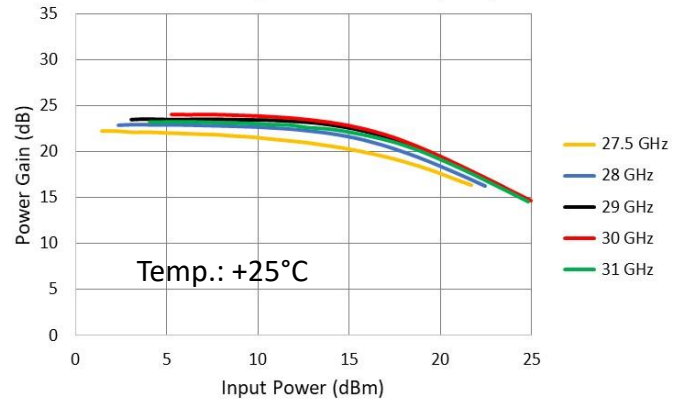
### Large Signal Performance

Test Condition:  $V_d = 26\text{ V}$ ,  $I_{dq} = 0.5\text{ A}$  at  $25^\circ\text{C}$ , CW Performance

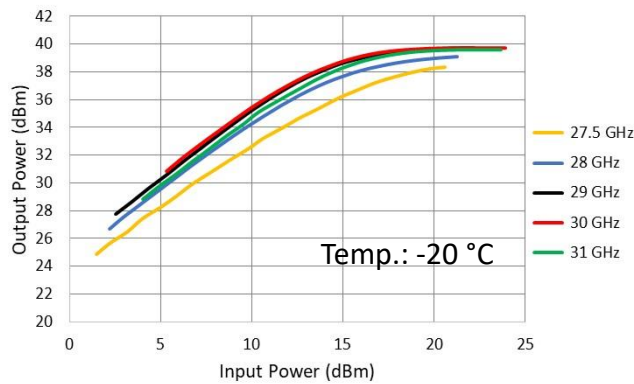
Output Power vs. Input Power vs. Frequency



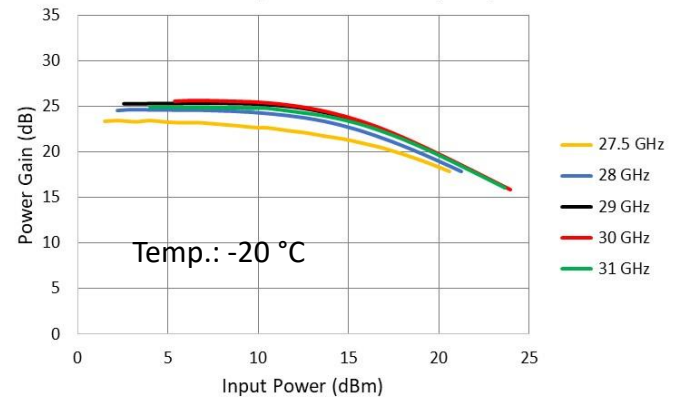
Power Gain vs. Input Power vs. Frequency



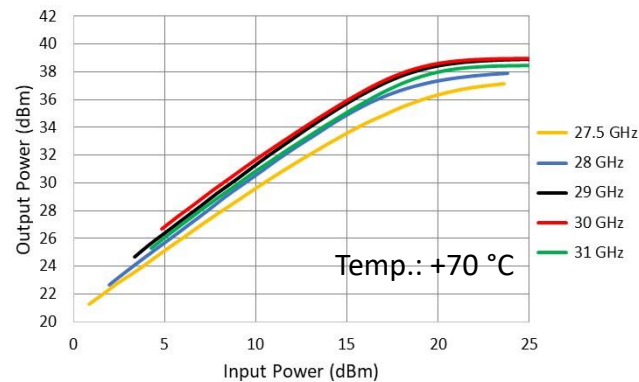
Output Power vs. Input Power vs. Frequency



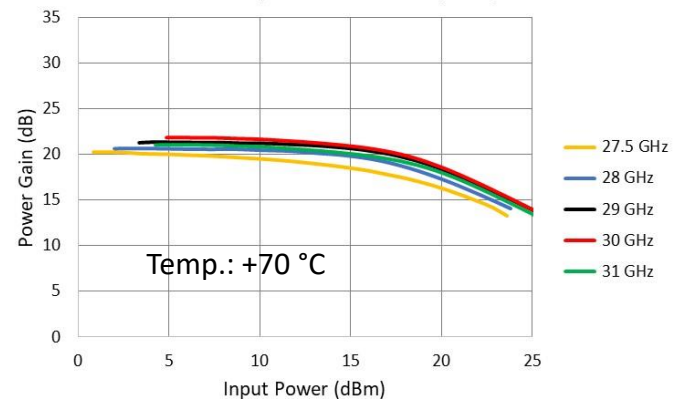
Power Gain vs. Input Power vs. Frequency



Output Power vs. Input Power vs. Frequency

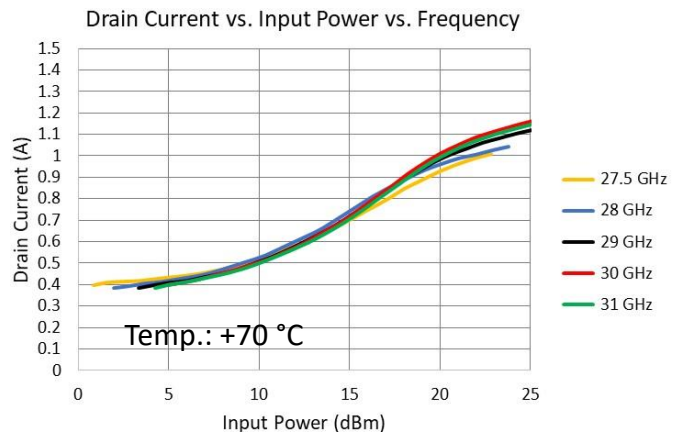
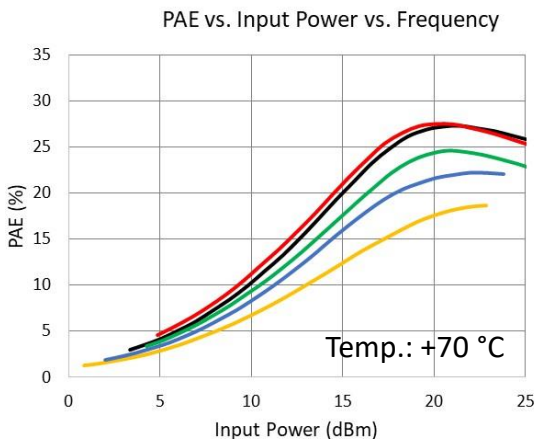
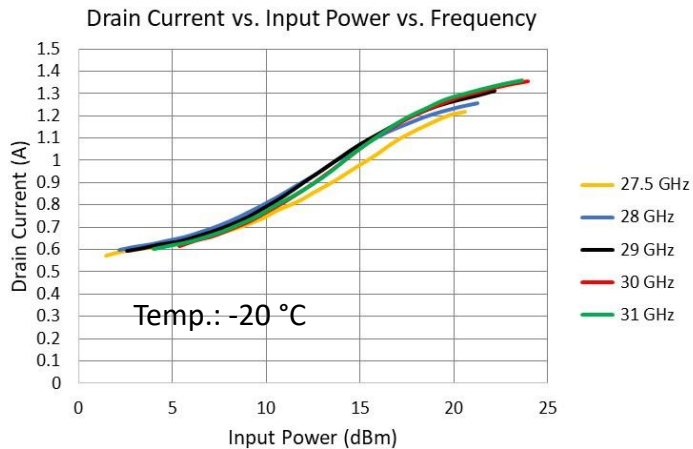
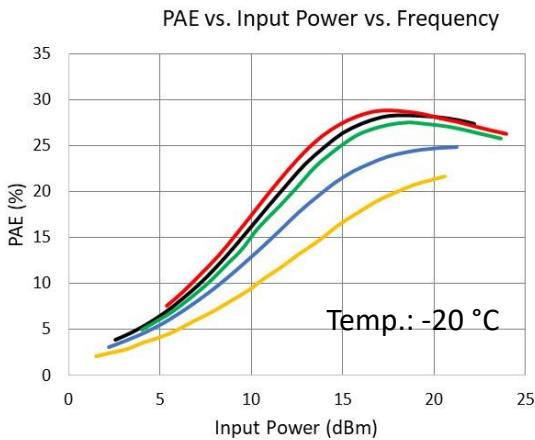
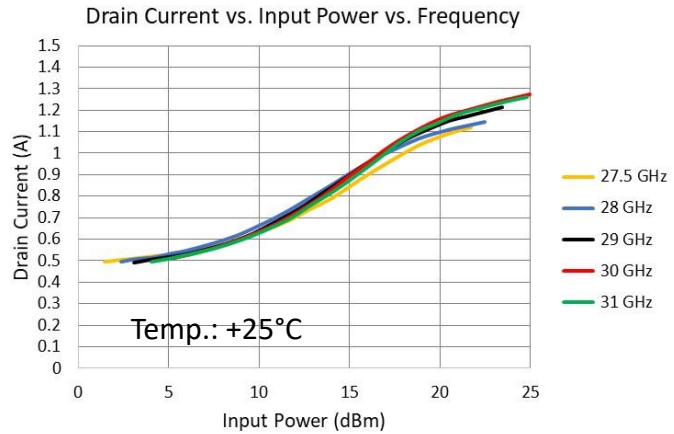
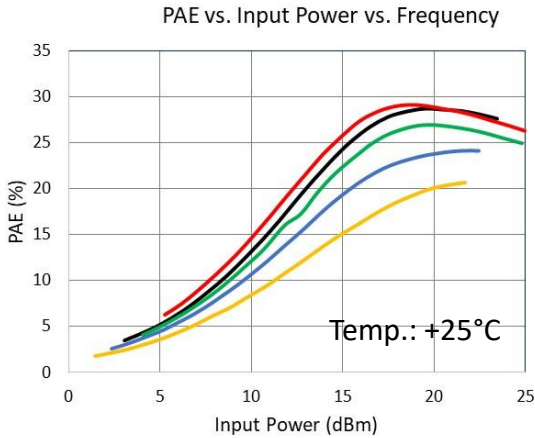


Power Gain vs. Input Power vs. Frequency



### Large Signal Performance

Test Condition:  $V_d = 26\text{ V}$ ,  $I_{dq} = 0.5\text{ A}$  at  $25^\circ\text{C}$ , CW Performance

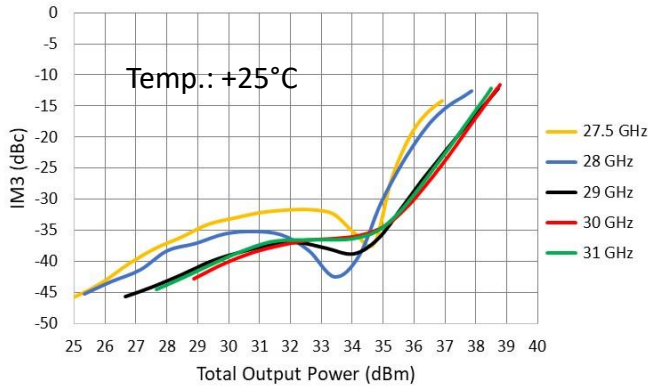


## 27.5 – 31 GHz GaN 8 W Power Amplifier

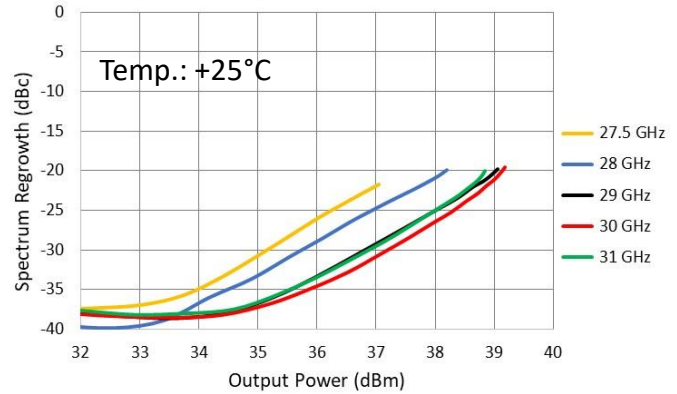
**2-Tone Linearity Performance vs. Temperature**  
 Test Condition:  $V_d = 26\text{ V}$ ,  $I_{dq} = 0.5\text{ A}$  at  $25^\circ\text{C}$   
 10 MHz Tone Spacing

**Spectral Regrowth Performance vs. Temperature**  
 Test Condition:  $V_d = 26\text{ V}$ ,  $I_{dq} = 0.5\text{ A}$  at  $25^\circ\text{C}$   
 QPSK, 10 MSPS, Alpha = 0.2

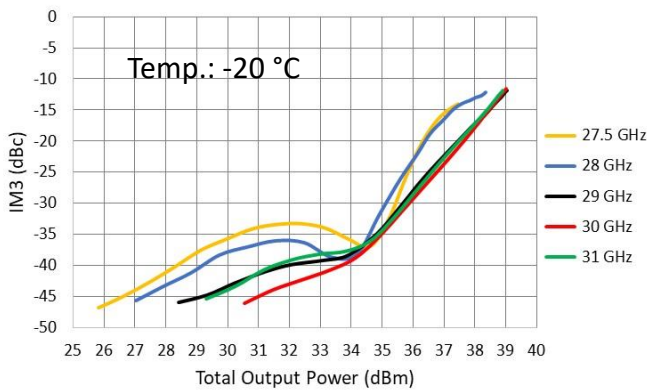
IM3 vs. Total Output Power



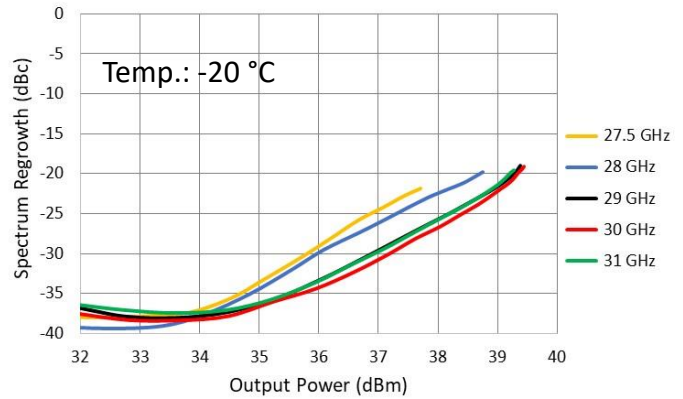
Spectrum Regrowth vs. Output Power



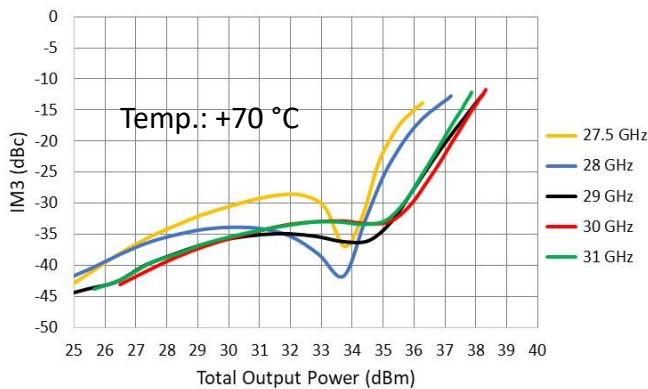
IM3 vs. Total Output Power



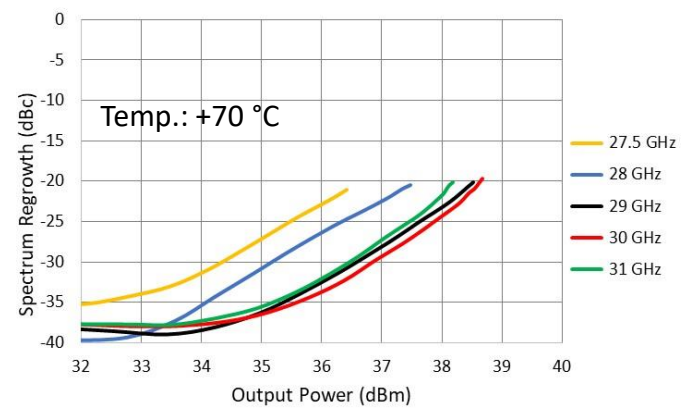
Spectrum Regrowth vs. Output Power



IM3 vs. Total Output Power



Spectrum Regrowth vs. Output Power

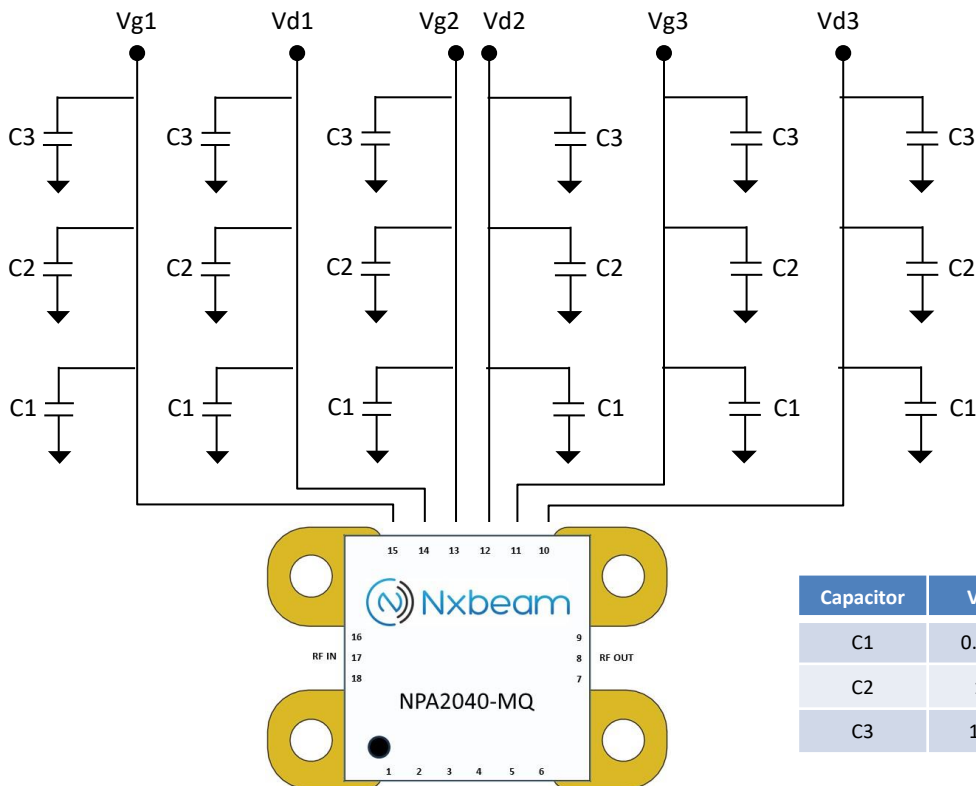




### Connection and Off-Chip Components

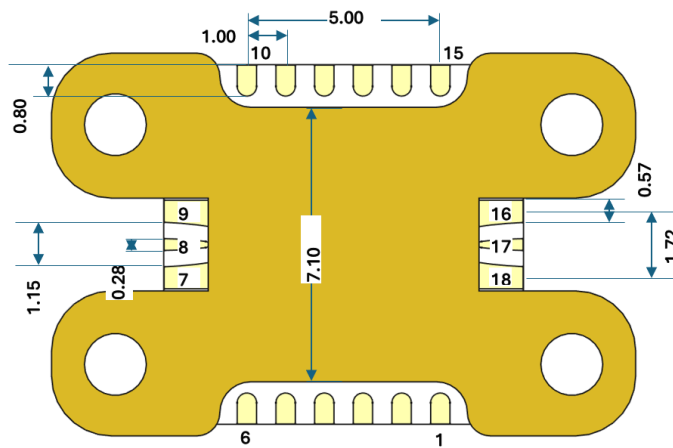
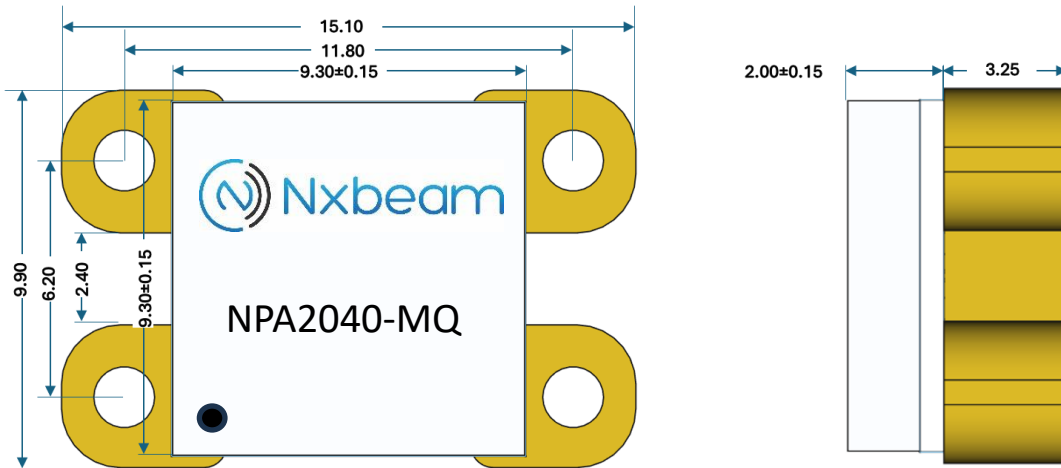
The following diagram shows the recommended off-chip components. The off-chip components should be located as close to the part as possible. Please consult with Nxbeam on other off-chip network variations.

Pad Num.	Function	Pad Num.	Function
1	NC	10	Vd3
2	NC	11	Vg3
3	NC	12	Vd2
4	NC	13	Vg2
5	NC	14	Vd1
6	NC	15	Vg1
7	GND	16	GND
8	RF OUT	17	RF IN
9	GND	18	GND



Capacitor	Value
C1	0.01 $\mu$ F
C2	1 $\mu$ F
C3	10 $\mu$ F

Dimensions  
(all dimensions in mm)





## Bias Information

### Bias-up Procedure:

- 1.) It is recommended that voltage and current limits are set on the voltage supply's prior to biasing the product.
- 2.) Ensure power supplies are properly grounded to the product test fixture.
- 3.) Apply a negative gate voltage of -7V to Vg1, Vg2, and Vg3 to ensure all devices are pinched off.
- 4.) Gradually increase the drain bias voltage (Vd1, Vd2, Vd3) to the desired bias level but not to exceed the maximum voltage of 28 V.
- 5.) Gradually increase the gate voltages (Vg1, Vg2, Vg3) while monitoring the drain current until the desired drain current in each stage is achieved.
- 6.) Apply RF signal.

### Bias-down Procedure:

- 1.) Turn off RF signal.
- 2.) Gradually decrease Vg1, Vg2, and Vg3 down to -7 V.
- 3.) Gradually decrease the drain voltages (Vd1, Vd2, Vd3) down to 0 V.
- 4.) Gradually increase gate voltages (Vg1, Vg2, Vg3) to 0 V.
- 5.) Turn off supply voltages

### ESD Sensitive Product



## Important Information

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